Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1 (currently amended): An audio processing circuit for receiving a first stream complying

5   with a first standard and generating a second stream complying with a second

standard which is a digital interface standard, the first stream includes a plurality of

frames, each of the frames includes a plurality of fields, the audio processing circuit

comprises:

a stream buffer for storing the frames of the first stream;

10   a stream recovering circuit electrically connected to the stream buffer for detecting

a copyright field in at least one of the plurality of fields in the frames,

modifying the copyright field to change copyright management information

at least one of the plurality of fields according to the first standard, and

generating modified frames;

15   a first buffer electrically connected to the stream recovering circuit for storing the

modified frames; and

a burst circuit electrically connected to the first buffer for partitioning the modified

frames into a plurality of payload sections, adding a preamble to each of the

payload sections, and forming the second stream.

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2 (original): The audio processing circuit of claim 1 wherein the second standard is

S/PDIF standard.

3 (original): The audio processing circuit of claim 1 wherein the first stream is retrieved

25   from an optical storage disk.

4 (original): The audio processing circuit of claim 1 further comprising:

a decoding circuit electrically connected to the stream buffer for decoding the

frames retrieved from the stream buffer;

a second buffer electrically connected to the decoding circuit for storing decoded

frames generated by the decoding circuit; and

5   a digital to analog converter electrically connected to the second buffer for

converting the decoded frames received from the second buffer to analog

signals.

5 (currently amended): The audio processing circuit of ~~claim 1~~ claim 4 wherein the

10   decoding circuit and the stream recovering circuit are integrated into an audio

processor of the audio processing circuit.

6 (currently amended): An audio processing circuit for receiving a first stream complying

with a first standard and generating a second stream complying with a second

15   standard which is a digital interface standard, the first stream includes a plurality of

frames, each of the frames includes a plurality of fields, the plurality of fields

include a sync word field, the audio processing circuit comprises:

a stream buffer for storing the frames of the first stream;

a stream recovering circuit electrically connected to the stream buffer for receiving

20   expected positions of the sync words derived from the first stream,

<u>determining if the expected positions of the sync words are correct,</u>

<u>repeatedly increasing the expected positions by one position when the</u>

<u>expected positions of the sync words are not correct,</u> locating actual positions

of the sync word fields ~~by detecting neighborhood positions substantially~~

25   ~~close to the expected positions~~, modifying the frames according to the actual

positions of the sync word fields, and generating modified frames;

a first buffer electrically connected to the stream recovering circuit for storing the

modified frames;

3

a burst circuit electrically connected to the first buffer for partitioning the modified

frames into a plurality of payload sections, adding a preamble to each of the

payload sections, and forming the second stream.

5   7 (original): The audio processing circuit of claim 6 wherein the second standard is

S/PDIF standard.

8 (original): The audio processing circuit of claim 6 wherein the first stream is retrieved

from an optical storage disk.

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9 (original): The audio processing circuit of claim 6 further comprising:

a decoding circuit electrically connected to the stream buffer for decoding the

frames retrieved from the stream buffer;

a second buffer electrically connected to the decoding circuit for storing decoded

15   frames generated by the decoding circuit; and

a digital to analog converter electrically connected to the second buffer for

converting the decoded frames received from the second buffer to analog

signals.

20   10 (currently amended): The audio processing circuit of ~~claim 6~~ claim 9 wherein the

decoding circuit and the stream recovering circuit are integrated into an audio

processor of the audio processing circuit.

11 (currently amended): A method for transferring a first stream complying with a first

25   standard into a second stream complying with a second standard which is a digital

interface standard, the first stream includes a plurality of frames, each of the frames

includes a plurality of fields, the method comprises the steps of:

detecting an audio mode field in ~~at least one of~~ the plurality of fields in the frames,

modifying the audio mode field ~~at least one of the plurality of fields according to the first standard~~, and generating modified frames; and

partitioning the modified frames into a plurality of payload sections, adding a preamble to each of the payload sections, and forming the second stream.

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12 (original): The method of claim 11 wherein the first stream is retrieved from an optical storage disk.

13 (original): The method of claim 11 wherein the second standard is S/PDIF standard.

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14 (currently amended): The method of claim 11 further comprising decoding the frames of [[the]] the first stream, and converting the decoded frames into analog signals.

15 (original): The method of claim 11 wherein the modifying step further comprises

15   omitting at least one redundant bit if any redundant bit exists in the frames of the first stream.

16-18 (cancelled).

20   19 (original): The method of claim 11 wherein the modifying step further comprises abandoning at least one improper bit which is not capable of being modified to conform with the first standard if any improper bit exists in the frames of the first stream.

25   20 (original): The method of claim 11 wherein the modifying step further comprises modifying errors in the fields of the frames of the first stream.